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	Application No.	Applicant(s)	
Notice of Allowability	10/046,103 Examiner	NUBER ET AL. Art Unit	
	Boris Benenson	2836	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in the or other appropriate communical RIGHTS. This application is sub	is application. If not included cation will be mailed in due co	urse <b>THIS</b>
1. This communication is responsive to 4/19/2004:	• • • • • • • • • • • • • • • • • • •		
2. The allowed claim(s) is/are 1 and 4-21.			
3. $\boxtimes$ The drawings filed on $\underline{11/01/2001}$ are accepted by the Ex	aminer.		•
4. ☐ Acknowledgment is made of a claim for foreign priority u  a) ☐ All b) ☐ Some* c) ☐ None of the:  1. ☐ Certified copies of the priority documents hav  2. ☐ Certified copies of the priority documents hav  3. ☐ Copies of the certified copies of the priority do  International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5. ☐ A SUBSTITUTE OATH OR DECLARATION must be subn	e been received. e been received in Application No pocuments have been received in the following of this communication to file a MENT of this application.  In the following of	No: I this national stage application reply complying with the requi	rements
6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") mu  (a) ☐ including changes required by the Notice of Draftsper  1) ☐ hereto or 2) ☐ to Paper No /Mail Date	st be submitted.		
(b) including changes required by the attached Examiner Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR	1.84(c)) should be written on the c	Irawings in the front (not the ba	ck) of
each sheet. Replacement sheet(s) should be labeled as such in a control of the deposit of and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATER	IAL must be submitted. Not	e the
Attachment(s)  1. ⊠ Notice of References Cited (PTO-892)		mal Patent Application (PTO-1	52)
<ol> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date</li></ol>	Paper No./Ma 08), 7. ☐ Examiner's Arr	il Date nendment/Comment	
of Biological Material  U.S. Patent and Trademark Office	9.	SHEGORY A TONTLEY, JR.	nce -
PTOL-37 (Rev. 1-04)	otice of Allowability	Part of Rapec No./Mail	Date 05052004

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## Detailed Actions

1. Amendment received on 4/19/2004 is entered.

- 2. The Specification (Page 5, Line 27 Page 6, Line 3) is amended.
- 3. Claims 1, 4-10, and 15 are amended. Claims 2 and 3 previously cancelled. Claims 1 and 4-21 are pending in the Application.

## Allowable Subject Matter

4. Claims 1 and 4-21 are allowed.

The following is an examiner's statement of reasons for allowance:

5. Independent Claims 1 is allowable because none of the prior art of record disclose an integrated circuit comprising a buffer for reducing delays on relatively long conductive signal lines wherein the buffer includes a protection diode connected to a gate of one transistor of a first inverter of the buffer to prevent the buffer from being damaged by too much electrical charge on the gate in combination with the other claim limitations.

Claims 4-9 are dependent on Claim 1 and therefore allowable.

6. Independent Claim 10 is allowable because none of the prior art of record disclose a method for preventing a buffer used for

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reducing delays on relatively long conductive signal lines of an IC wherein the buffer includes a protection diode connected to a gate of one transistor of a first inverter of the buffer to prevent the buffer from being damaged by too much electrical charge on the gate and wherein the buffer and the protecting diode are formed on the IC in combination with the other claim limitations.

Claims 11-20 are dependent on Claim 10 and therefore allowable.

- 7. Independent Claim 21 is allowable because none of the prior art of record disclose a method for use in designing an integrated circuit comprising a step of inserting buffer cells for reducing delays on relatively long conductive signal lines wherein each buffer cell comprising first and second inverters and a protection diode connected to an input of the first inverter inside of the IC in combination with the other claim limitations.
- 8. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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## Contact information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Boris Benenson whose telephone number is (571) 272-2048. The examiner can normally be reached on M-F (8:20-6:00) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571) 272-2800 x 36. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Boris Benenson Examiner Art Unit 2836

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